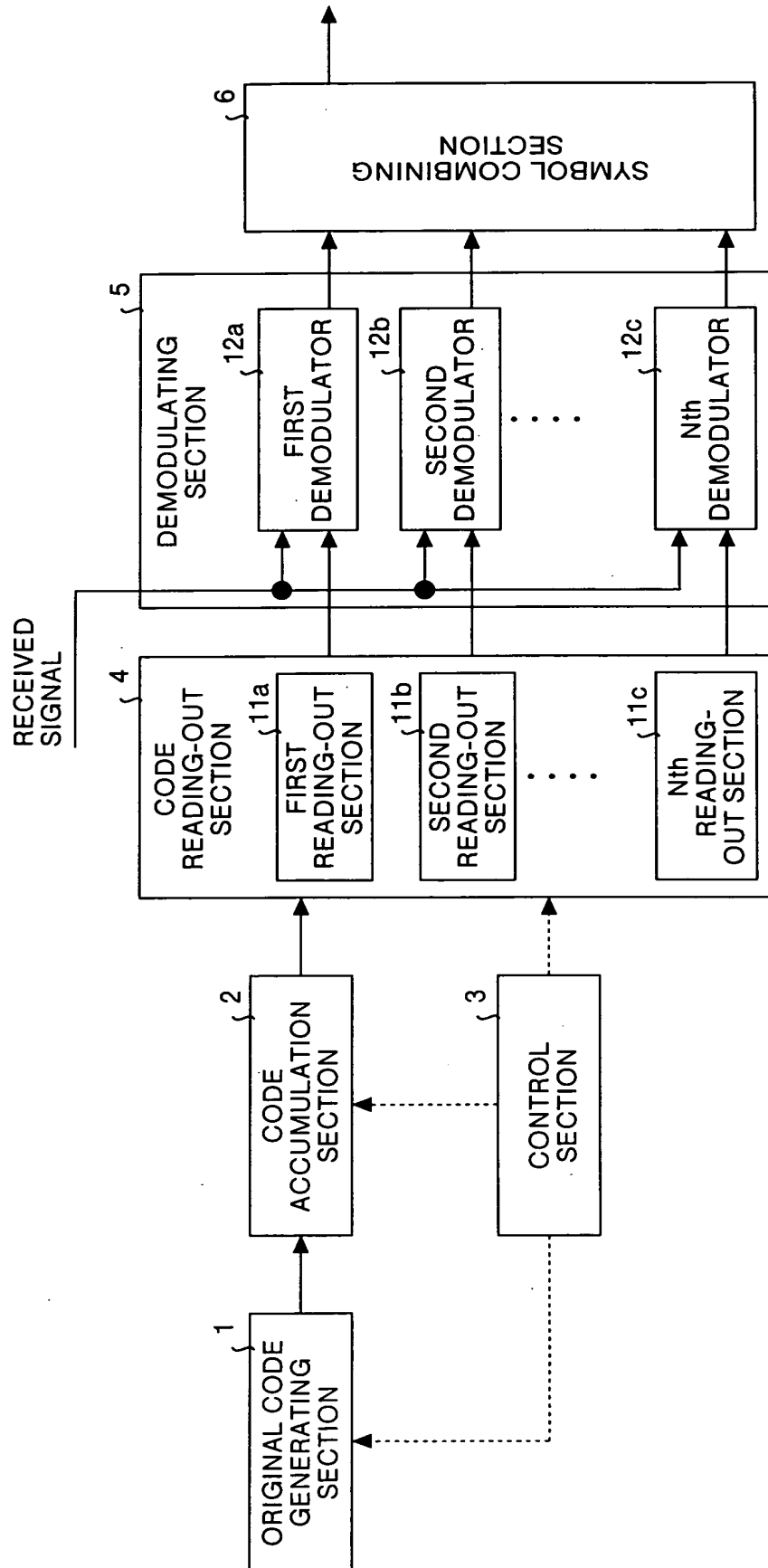


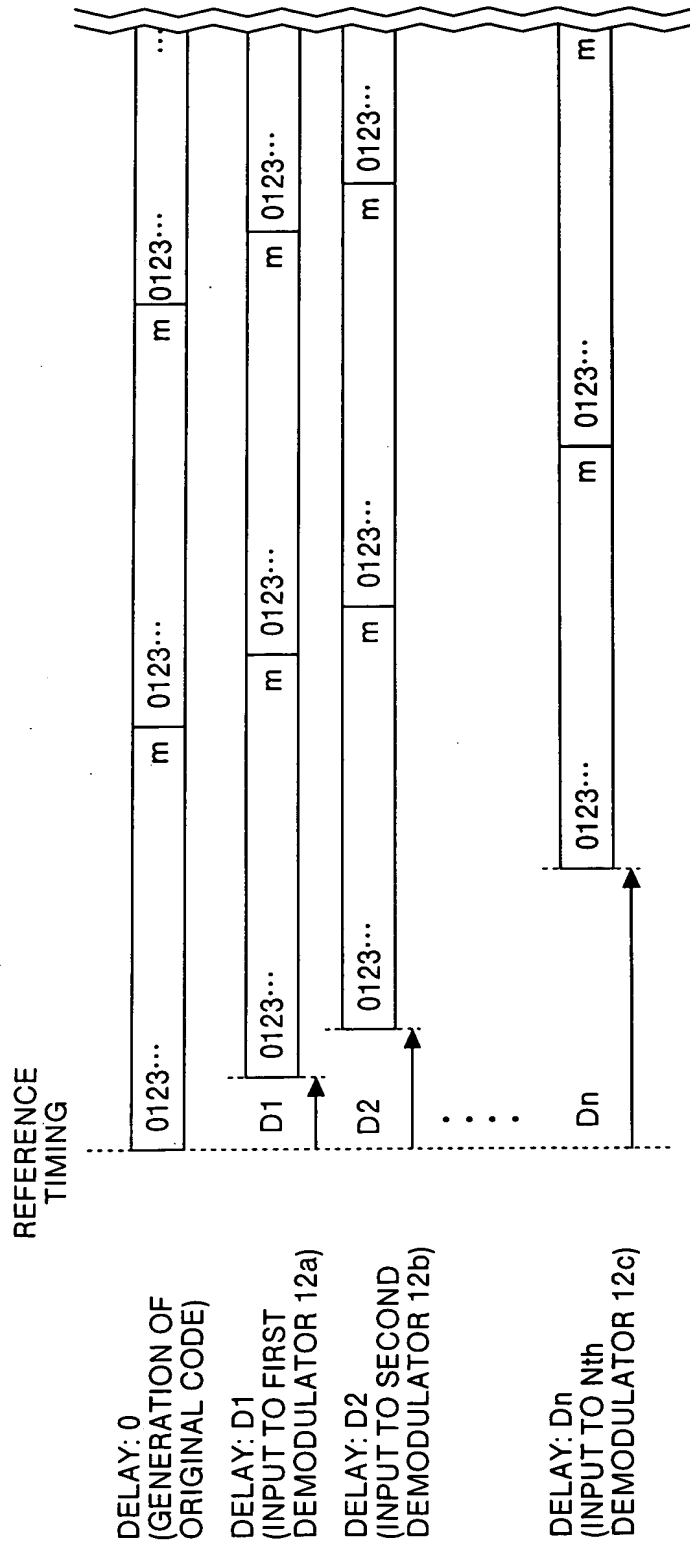
1/14

FIG.1



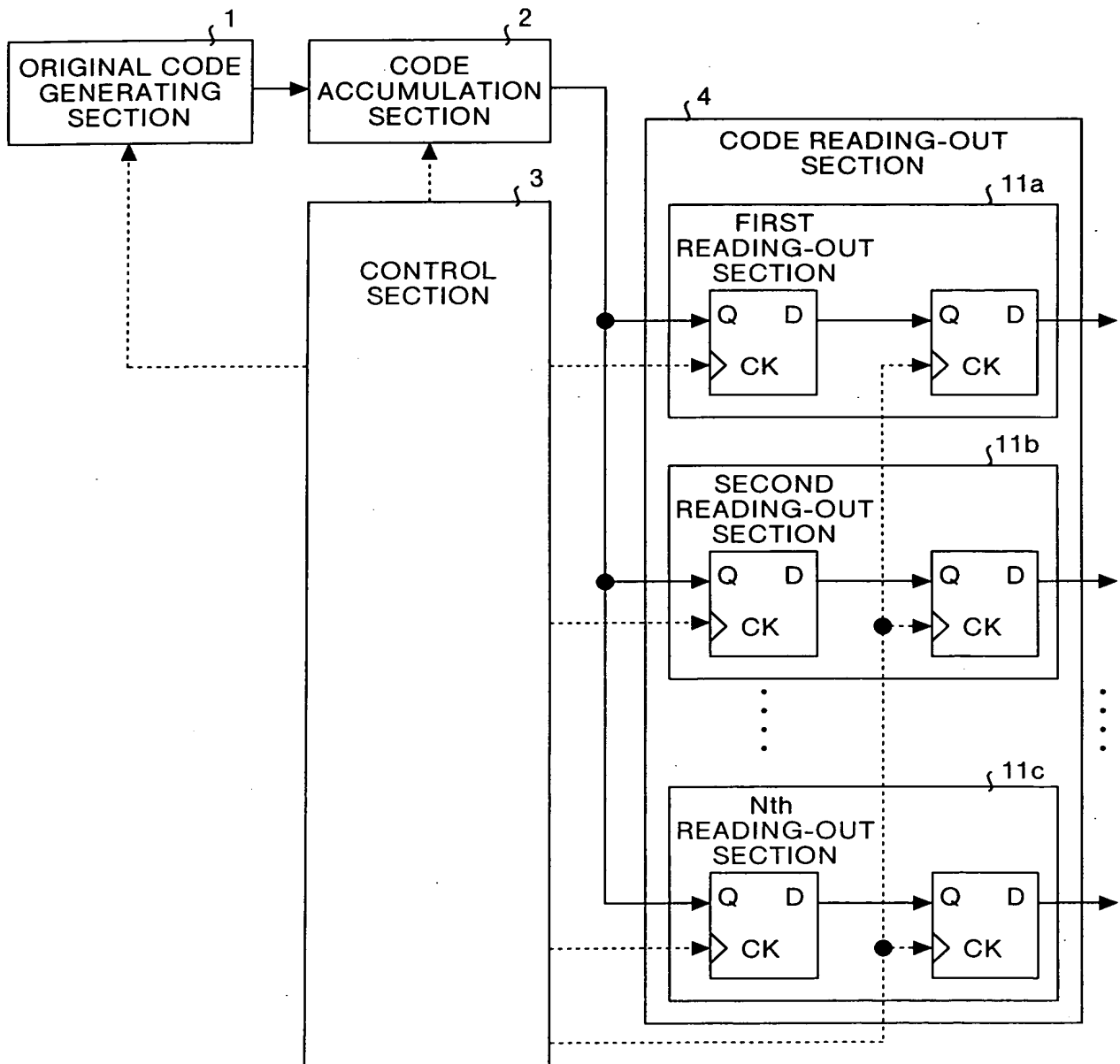
2/14

FIG.2



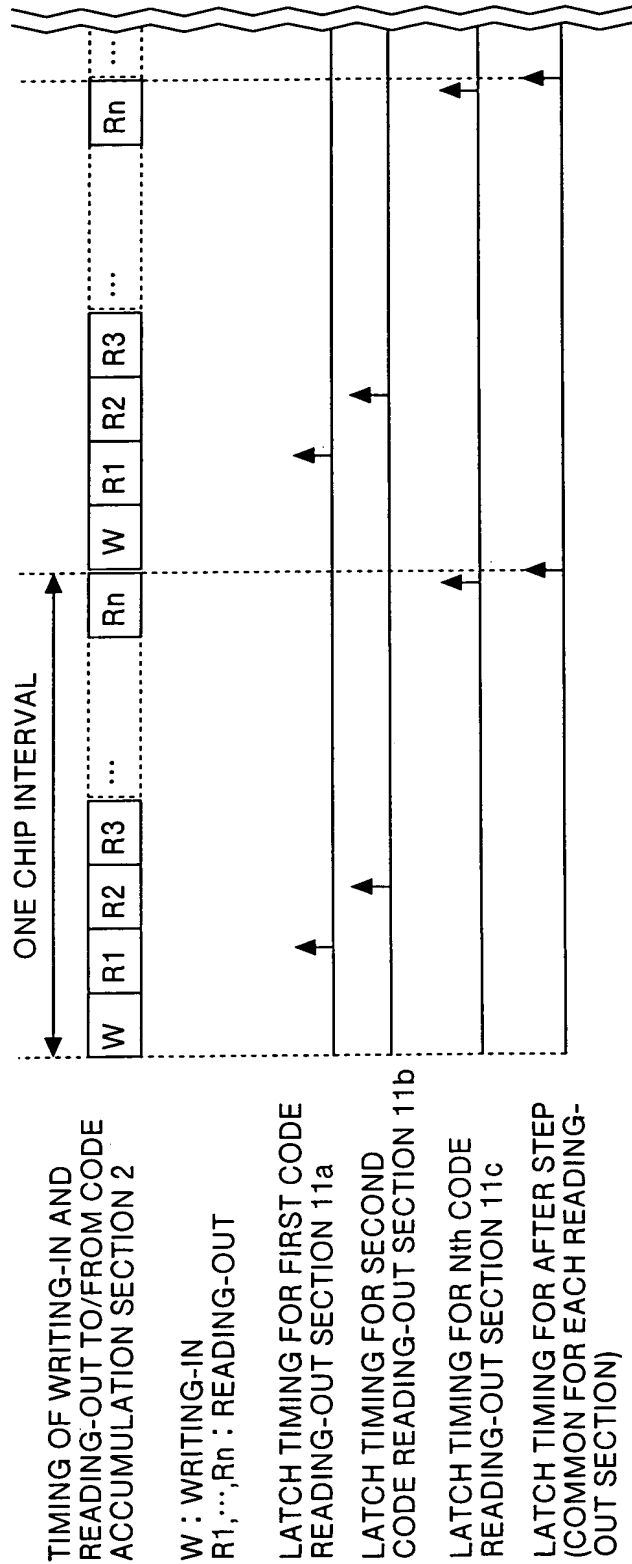
3/14

FIG.3



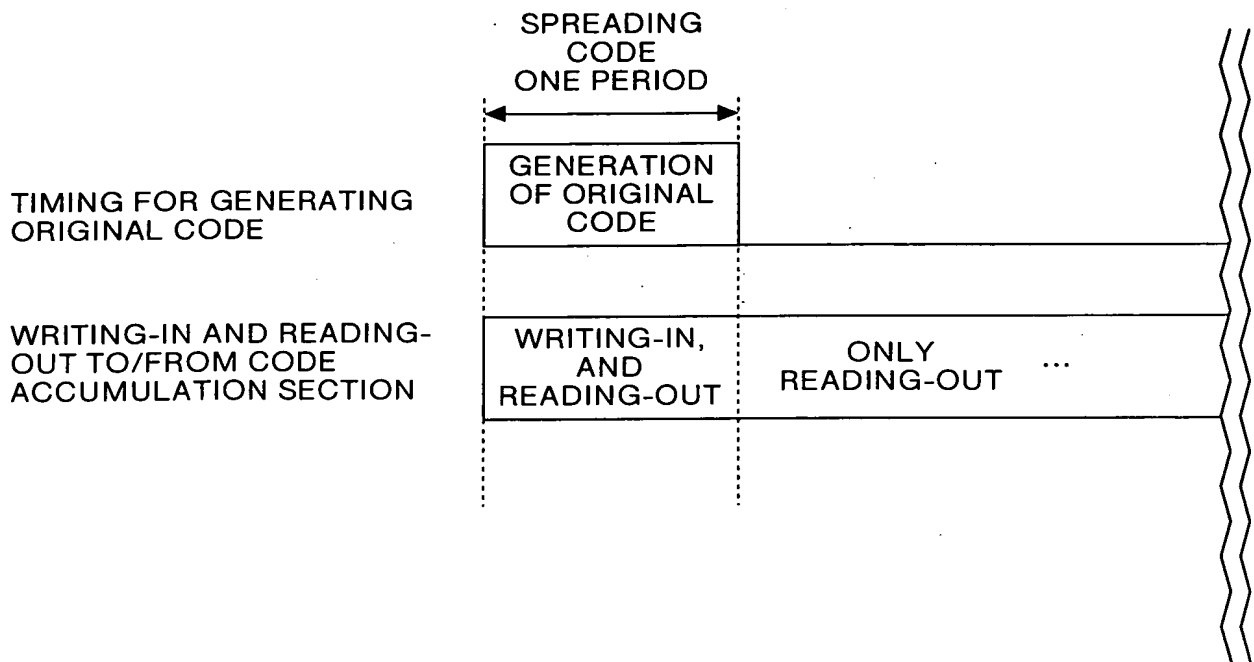
4/14

FIG.4



5/14

FIG.5



6/14

FIG.6

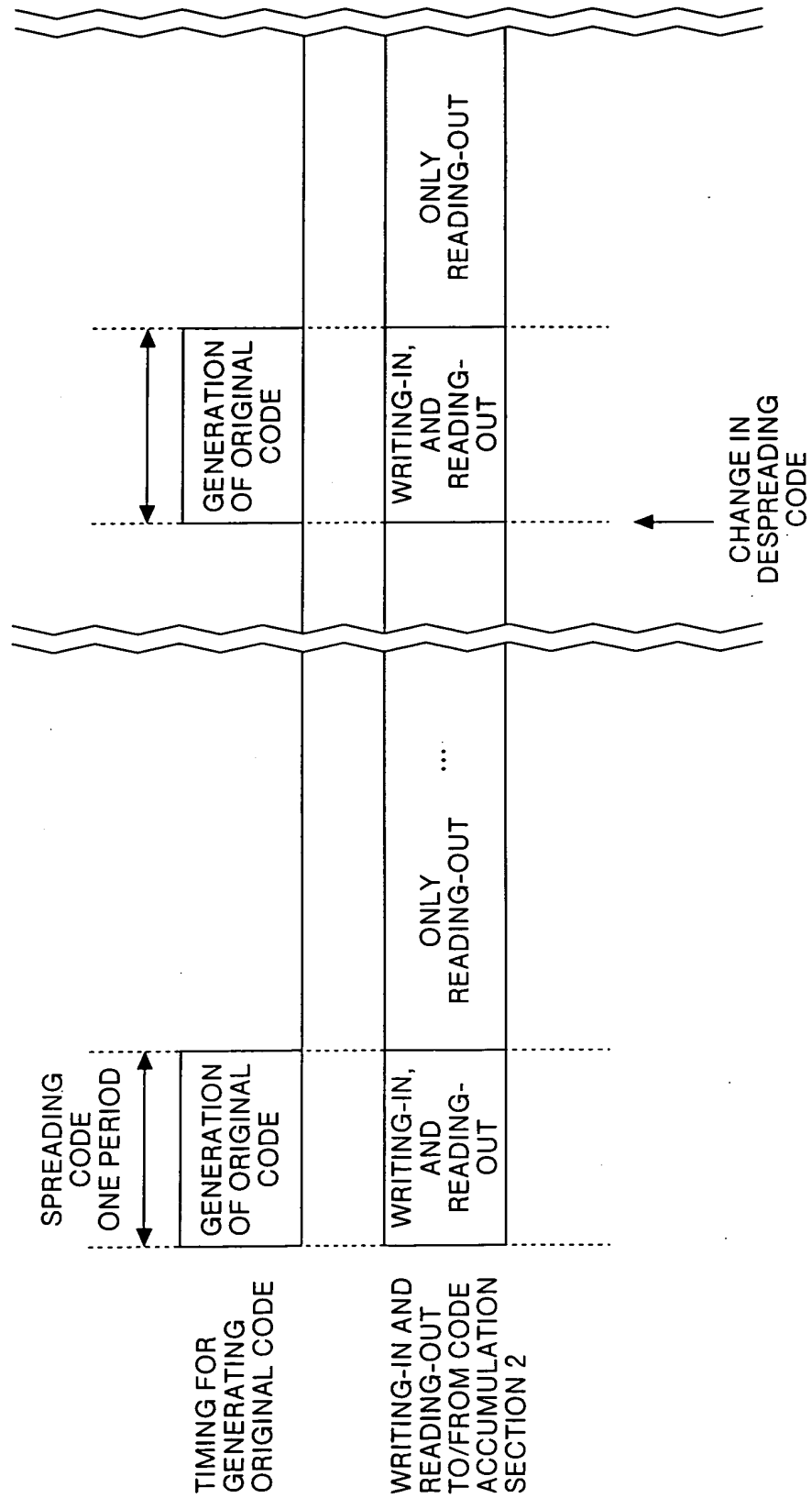
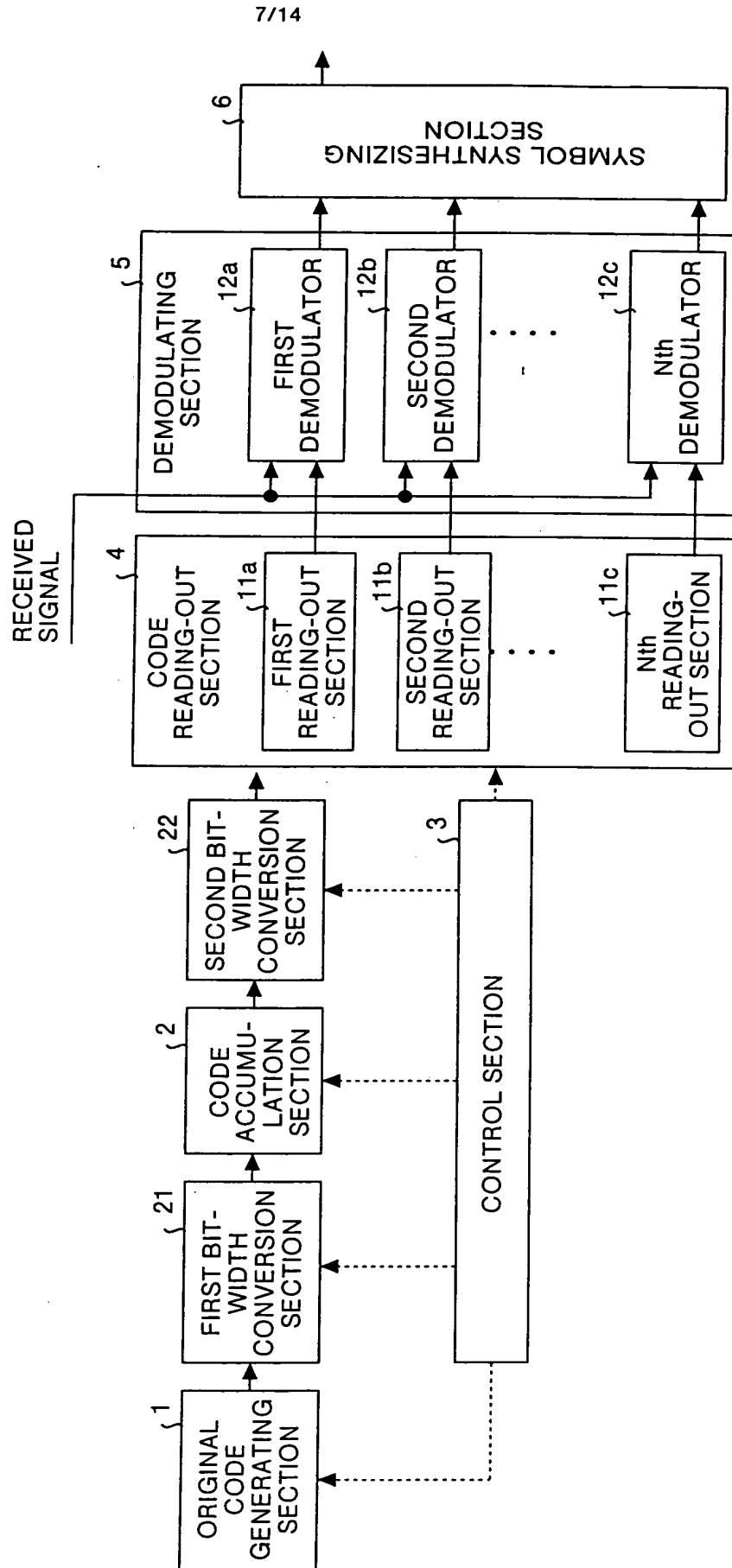
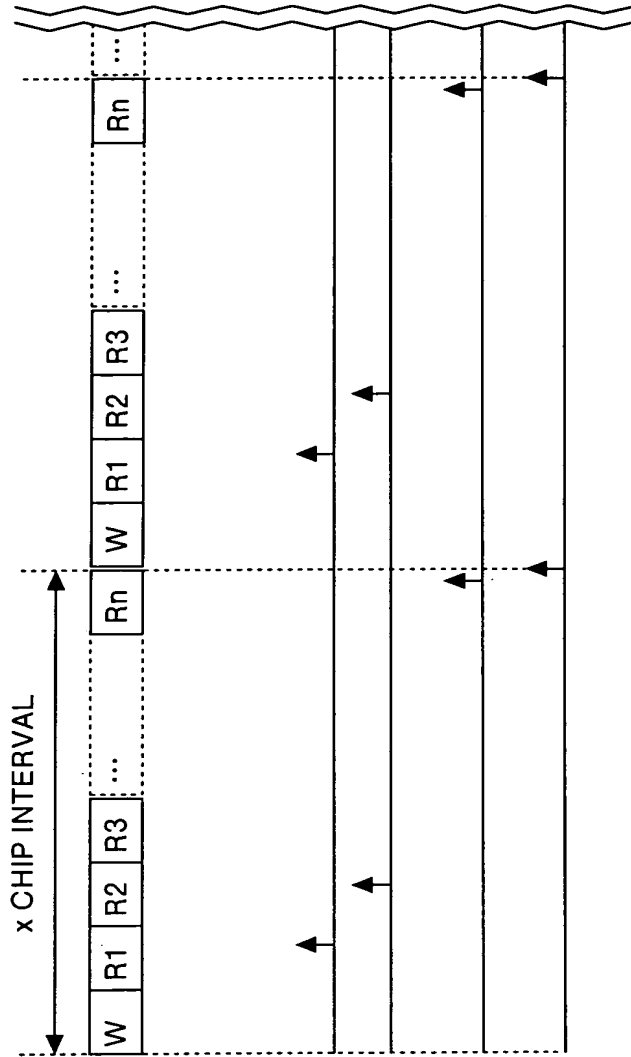


FIG. 7



8/14

FIG.8



WRITING-IN AND READING-OUT
TO/FROM CODE ACCUMULATION
SECTION

W : WRITING-IN
R1, ..., Rn : READING-OUT

LATCH TIMING FOR FIRST CODE
READING-OUT SECTION 11a

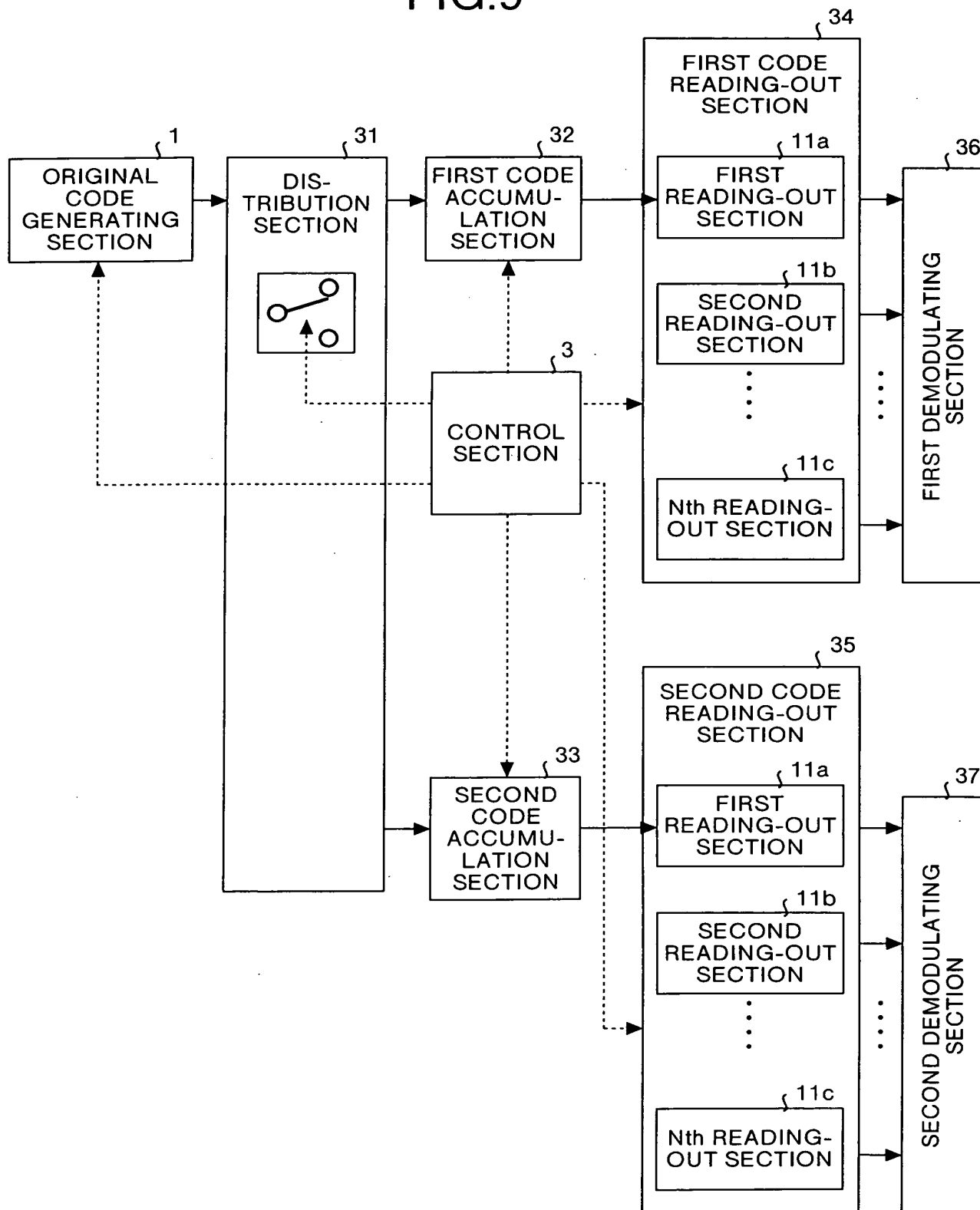
LATCH TIMING FOR SECOND CODE
READING-OUT SECTION 11b

LATCH TIMING FOR Nth CODE
READING-OUT SECTION 11c

LATCH TIMING FOR AFTER STEP
(COMMON FOR EACH READING-
OUT SECTION)

9/14

FIG.9



10/14

FIG.10

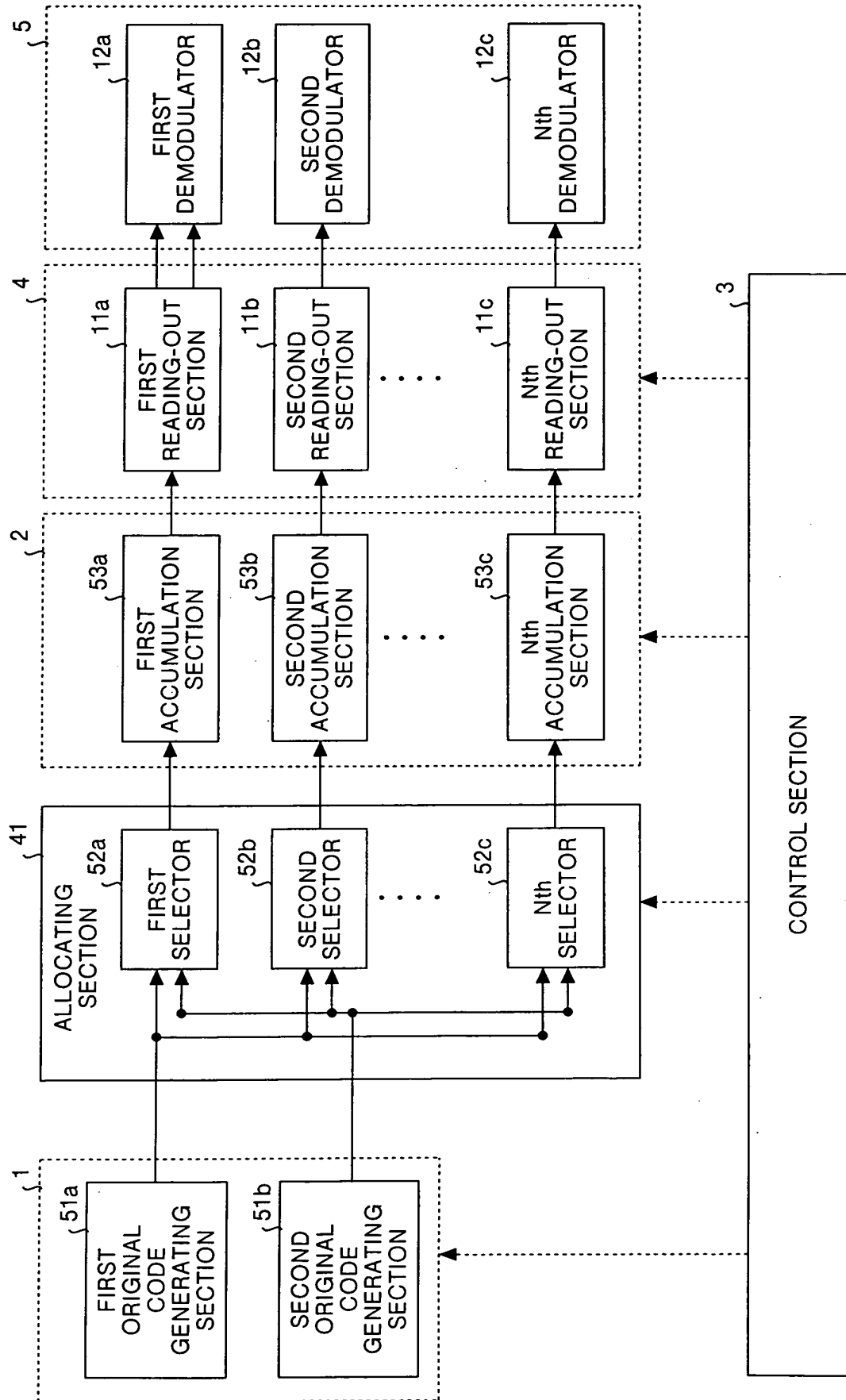
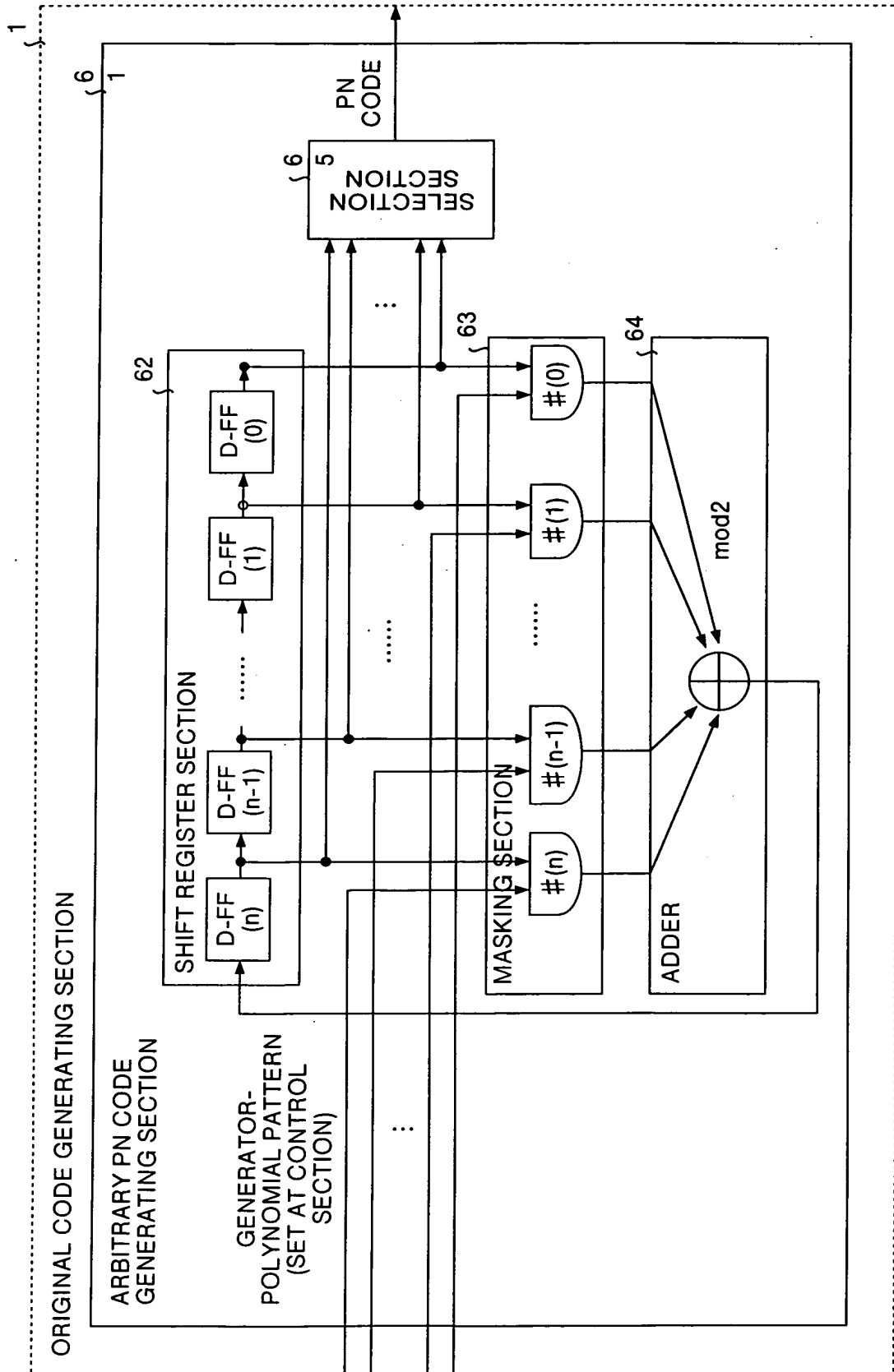
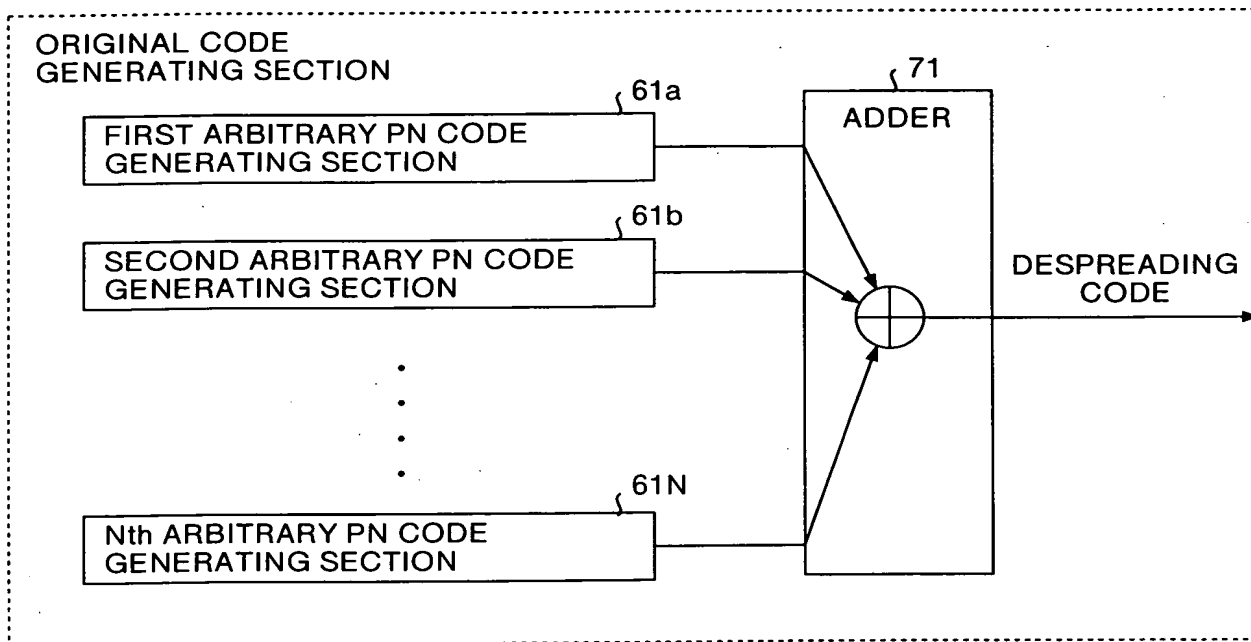


FIG.11



12/14

FIG.12



13/14

FIG.13

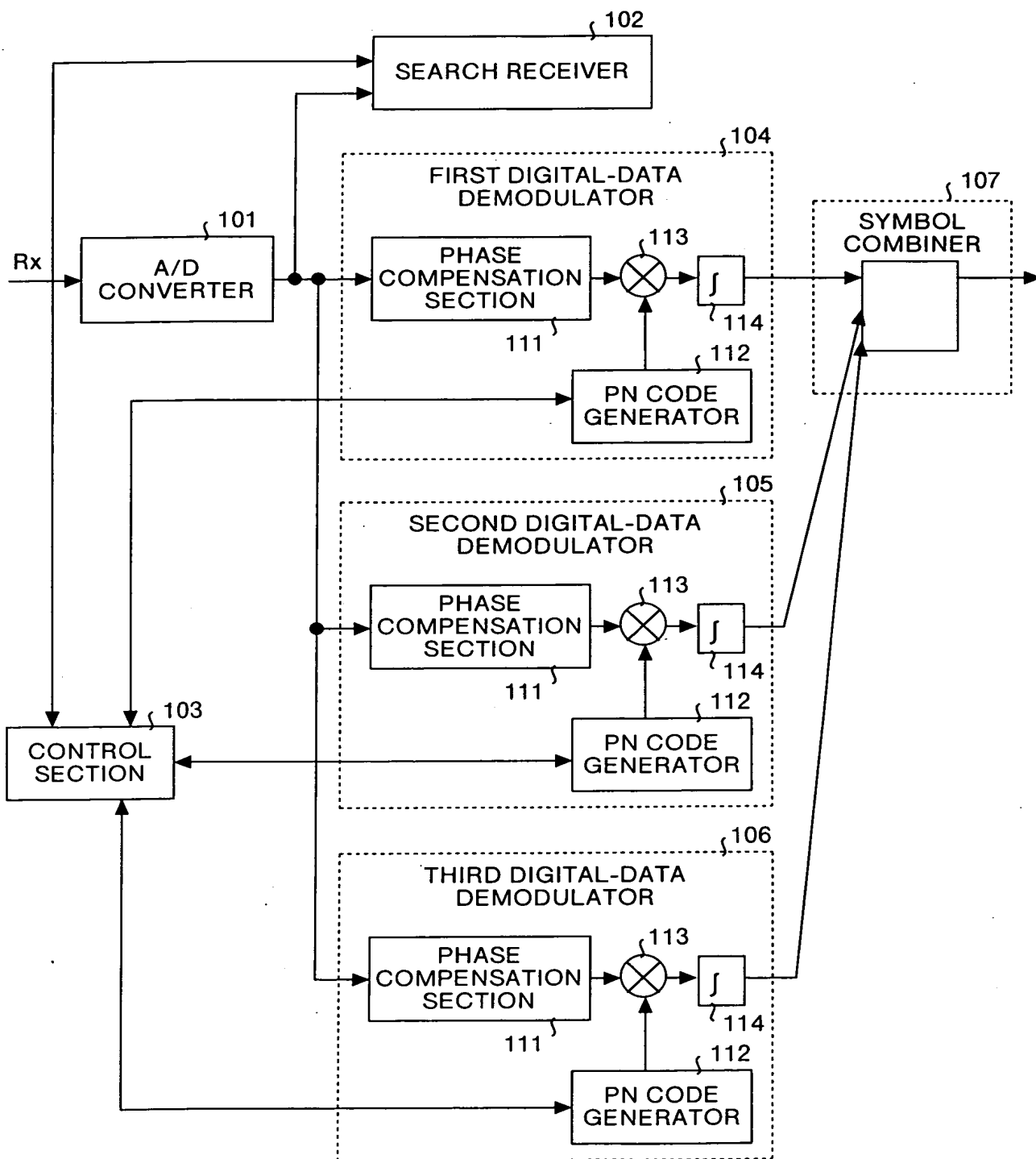


FIG.14

